Weekly Progress Report

Week ending 11/9/2014

1. Compared Print out scale version of board layout to actual components and made necessary adjustments.
2. Revised board layout, and debugged logo issues to have board sent out to PCB manufacture.
3. Helped debug new revision of code with prototyped circuit.

Tasks to Be Completed Next Week:

1. Begin drafting the housing for the board, make cad file.
2. Assemble boards with actual components, and debug any issues that might arise.
3. Create documentation lock operation algorithm.